

Attorney's Docket No.: 11103-017001 / PU01-0054

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Ken Miyagawa, et al. Art Unit : 2771
Serial No. : 09/607,336 Examiner : Unknown
Filed : June 30, 2000
Title : BLOCK FORMING METHOD AND APPARATUS OF DIGITAL BIT STREAM

Attention: Official Draftsman
Commissioner for Patents
Washington, D.C. 20231

TRANSMITTAL OF FORMAL DRAWINGS

Please substitute the enclosed fourteen (14) sheets of formal drawings for the corresponding drawings presently in the application.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

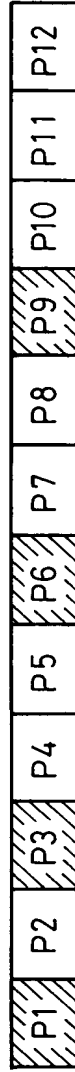
Date: _____

October 20, 2000

William D. Hare

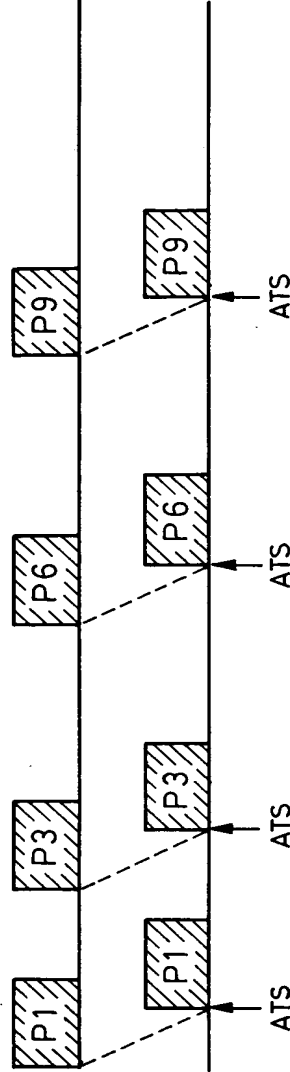
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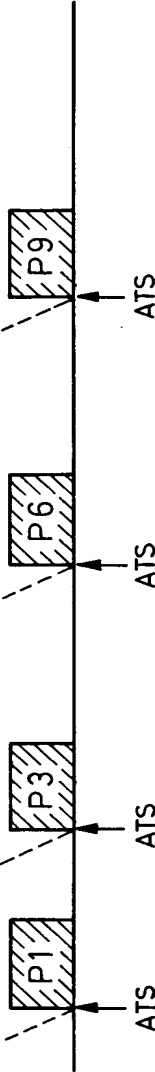
FULL TS

FIG. 1A



PARTIAL TS

FIG. 1B

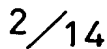


ADDITION OF
TIME STAMP

FIG. 1C



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The diagram illustrates the structure of a packet, divided into two main sections: the **MAIN DATA PORTION** and the **STREAM ID INFORMATION** portion.

The **MAIN DATA PORTION** is the larger section on the left, containing the primary data of the packet.

The **STREAM ID INFORMATION** portion is a smaller section on the right, which is further detailed as containing:

- REMAINDER OF THE PREVIOUS PACKET**: This section contains the data from the previous packet that did not fit into its main data portion. It is shown as a sequence of **TS** (Time Slice) and **PACKET** blocks.
- FIRST PORTION OF THE NEXT PACKET**: This section contains the data from the next packet that fits into the remaining space of the current packet's stream ID information. It is also shown as a sequence of **TS** and **PACKET** blocks.

The diagram shows that the **STREAM ID INFORMATION** portion is a continuation of the data stream from the previous packet and the beginning of the next packet, ensuring data integrity and continuity across packet boundaries.

FIG. 4

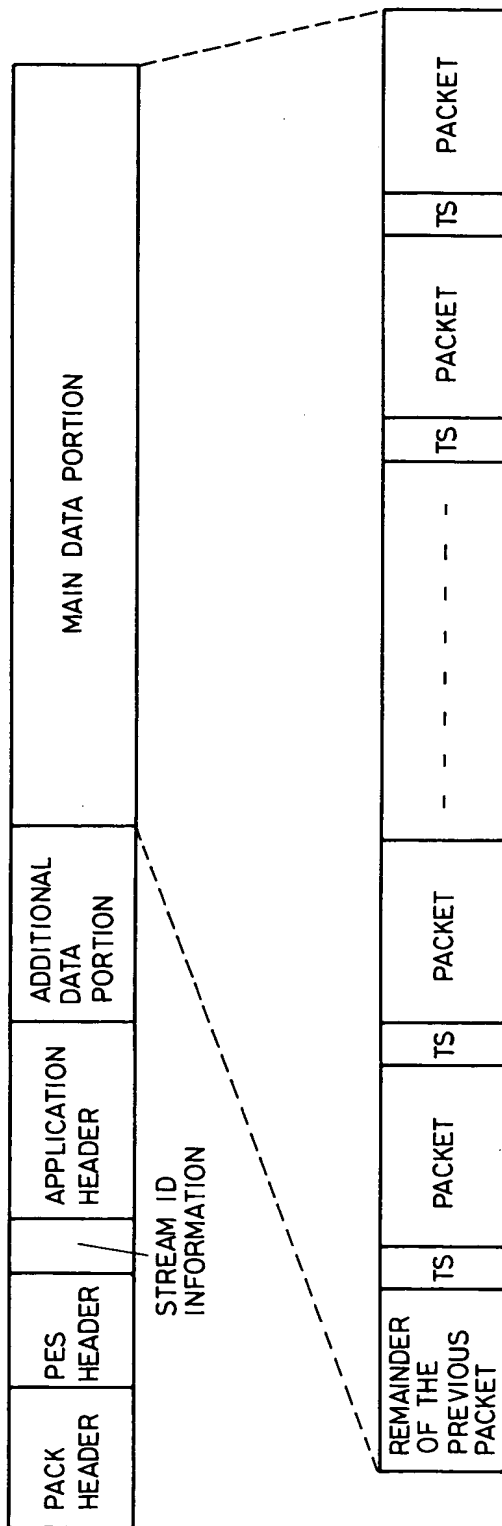


FIG. 5

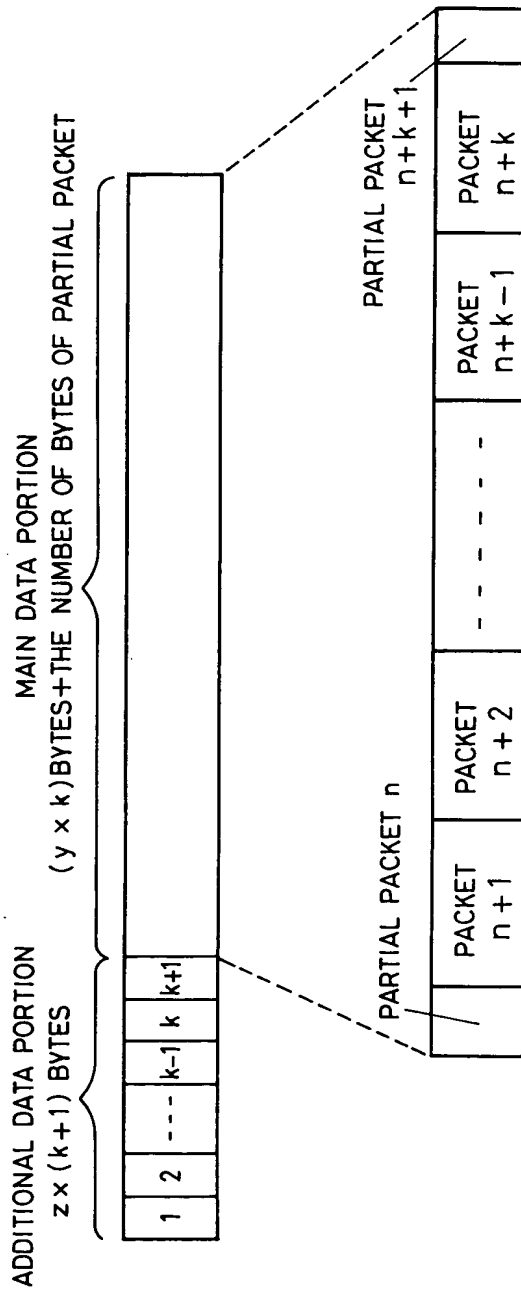


FIG. 6

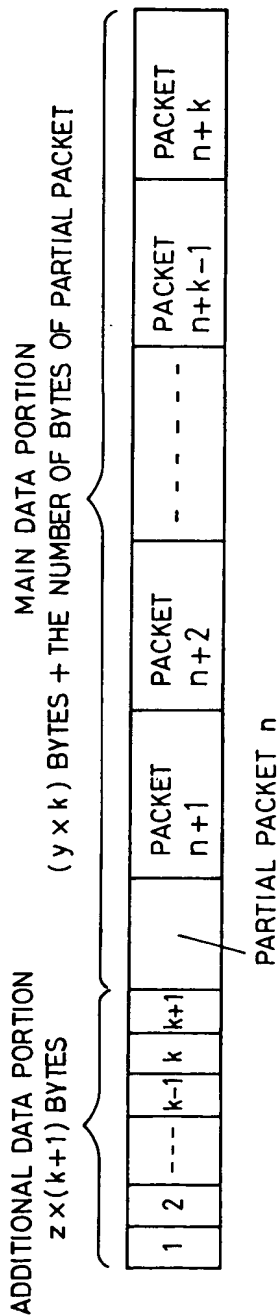


FIG. 7

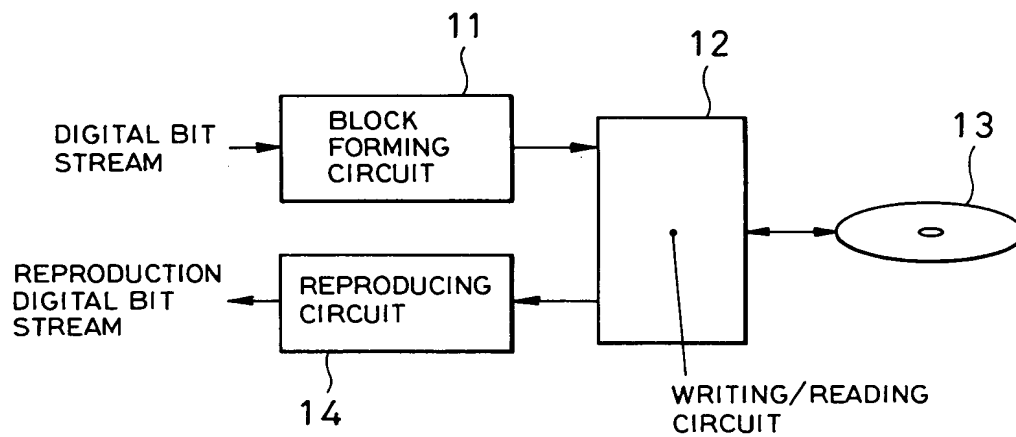


FIG. 8

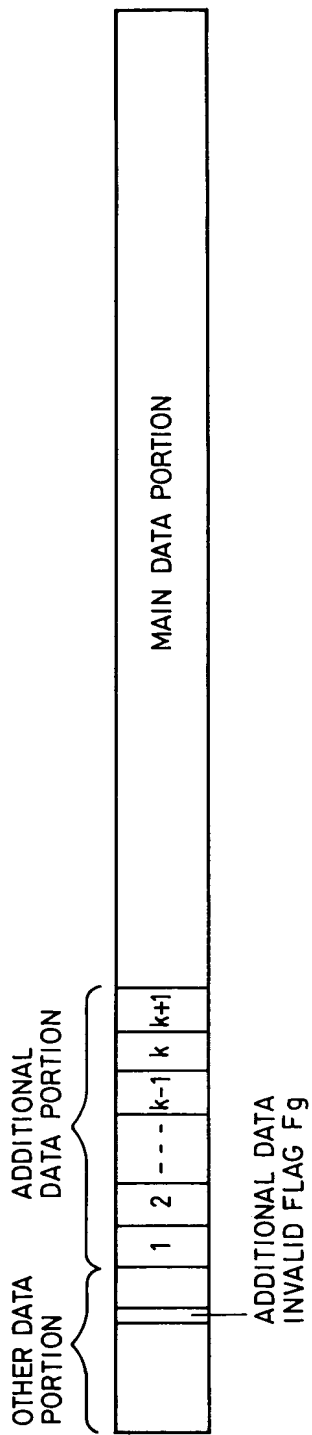


FIG. 9

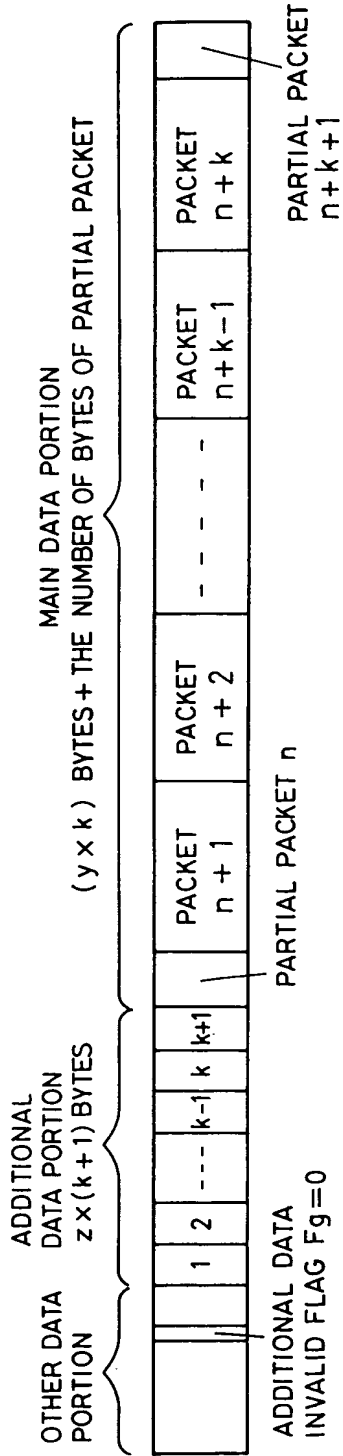


FIG. 11

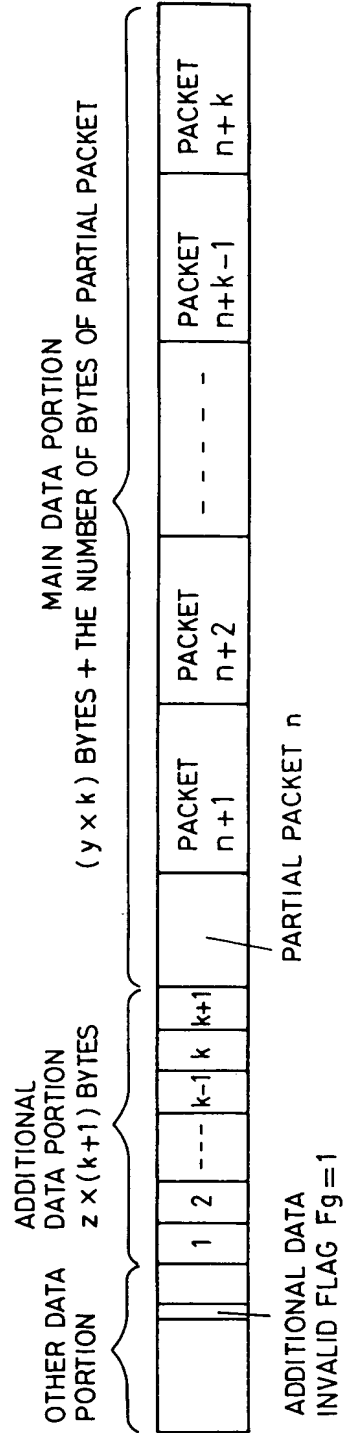


FIG.10

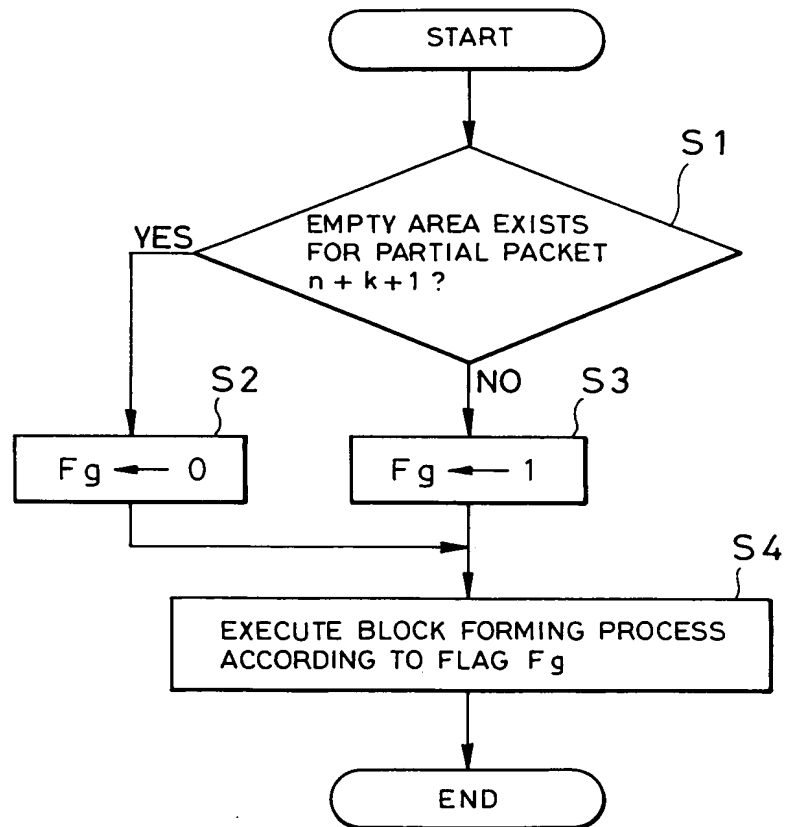


FIG.12

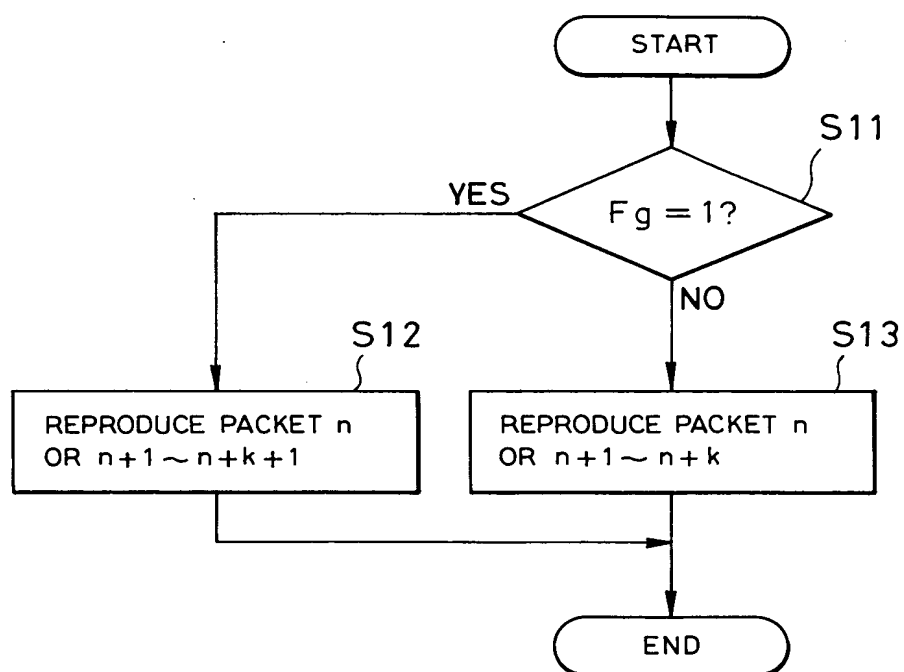


FIG.13

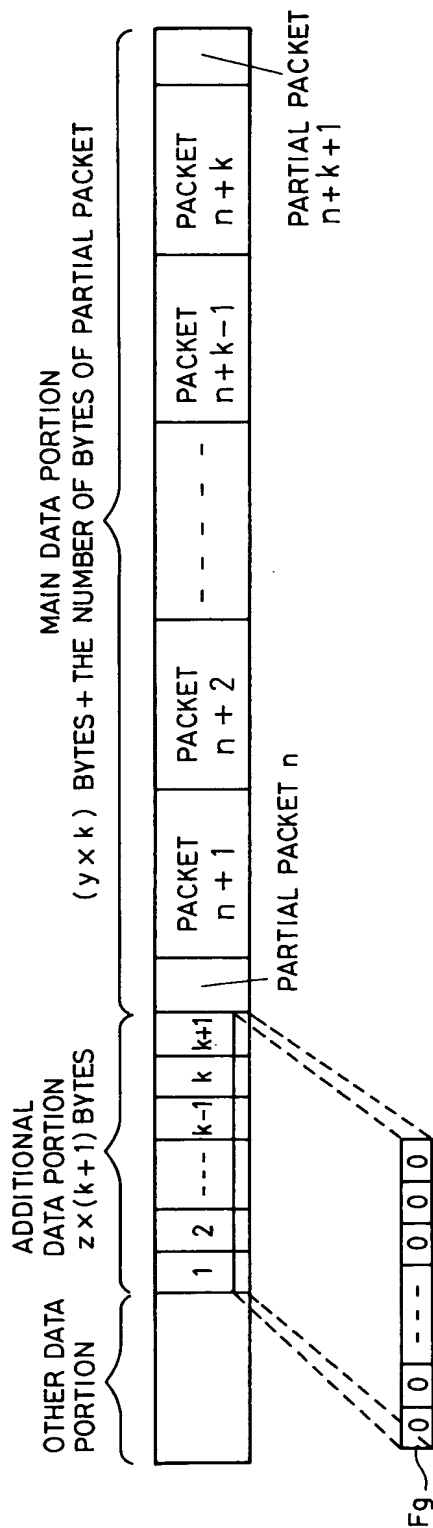


FIG.14

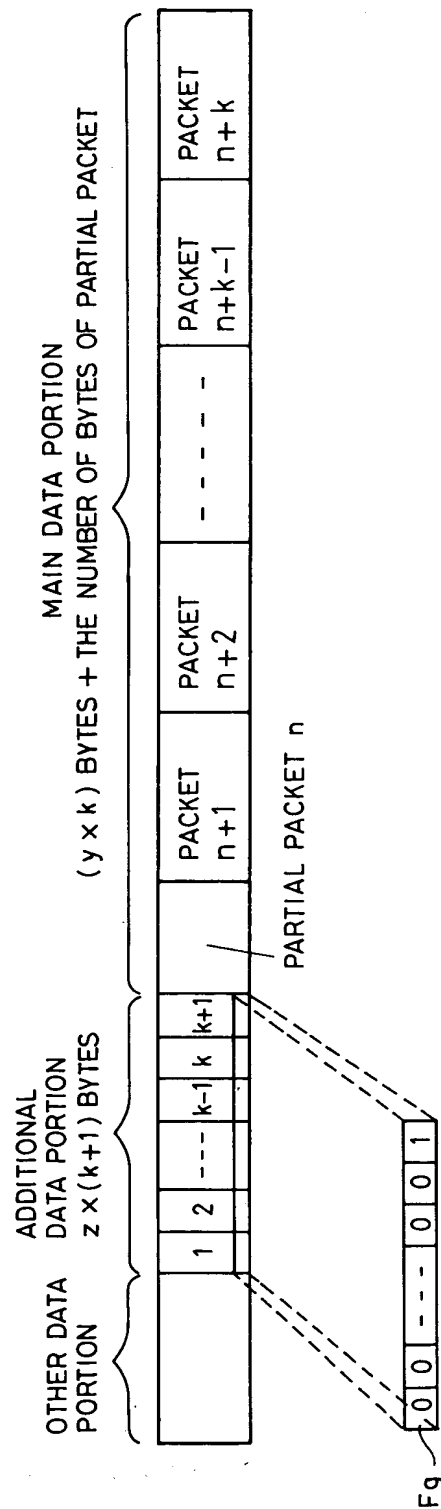


FIG.15

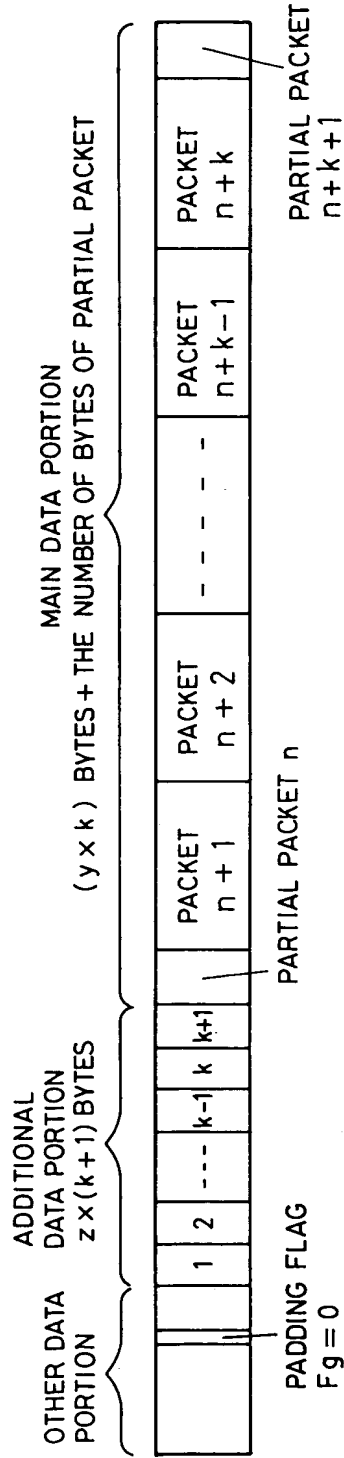


FIG.16

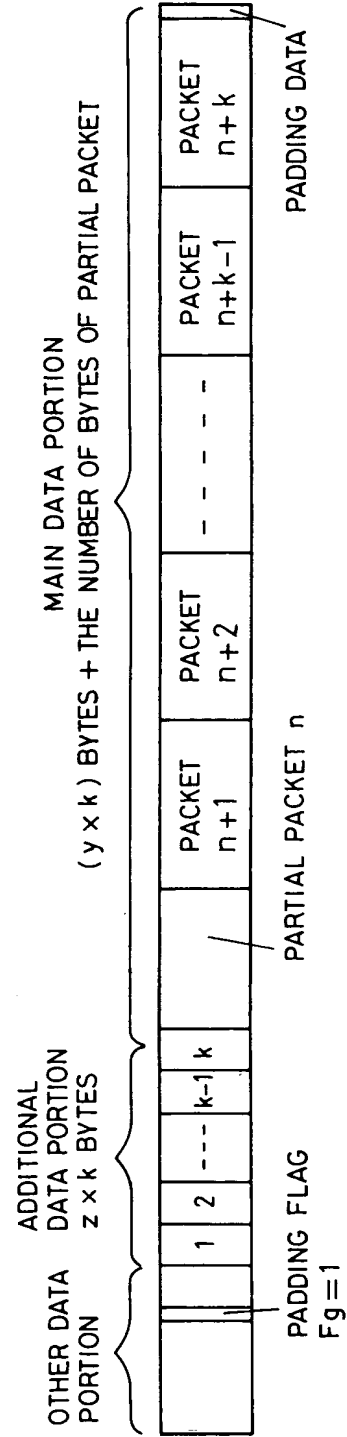
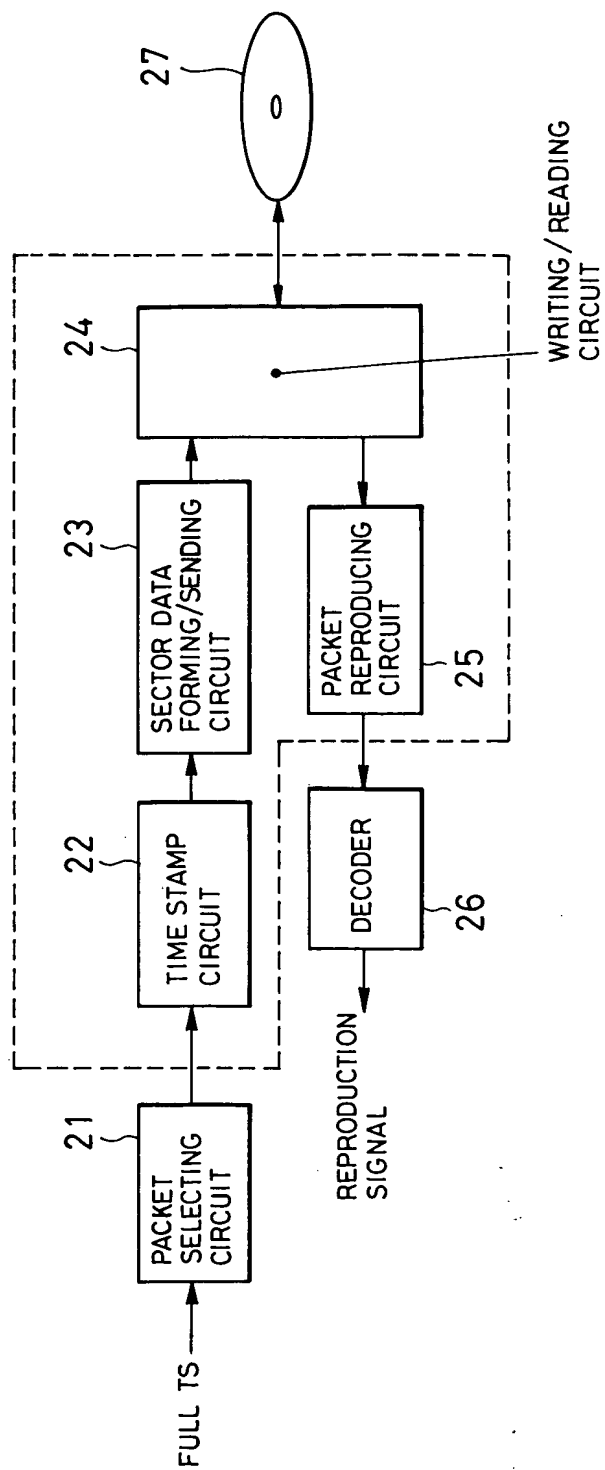


FIG.17



APPLICATION HEADER

